

R E M A R K S

Careful review and examination of the subject application are noted and appreciated.

SUPPORT FOR THE CLAIM AMENDMENTS

Support for the claim amendments may be found in the specification, for example, on page 7 line 11-page 8 line 16, page 16 line 9-page 18 line 5 and FIGS. 3, 4 and 6, as originally filed. Thus, no new matter has been added.

OBJECTION TO THE CLAIMS

The objection to claims 16 and 17 for informalities has been obviated by amendment and should be withdrawn.

CLAIM REJECTIONS UNDER 35 U.S.C. §102

The rejection of claims 11, 19 and 20 under 35 U.S.C. §102(b) as being anticipated by Hoogenboom et al. '387 (hereafter Hoogenboom) has been obviated in part by amendment, is respectfully traversed in part, and should be withdrawn.

Hoogenboom concerns a method and apparatus for efficient addressing of DRAM in a video decompression processor (title).

Claim 11 provides (A) dividing a first picture from the video signal into a plurality of picture segments and (B) dividing each one of the picture segments into a plurality of tiles. In

contrast, Hoogenboom appears to be silent regarding the rectangles 72 (alleged claimed picture segments) being further subdivided into tiles. Therefore, Hoogenboom does not appear to disclose or suggest (A) dividing a first picture from the video signal into a plurality of picture segments and (B) dividing each one of the picture segments into a plurality of tiles, as presently claimed. Claim 20 provides similar language.

Claim 11 further provides (C) generating a list associating each one of the tiles to a corresponding one page of a plurality of pages in a corresponding one bank of a plurality of banks in a memory such that each one of the picture segments has (a) at least a first one of the tiles associated with a first of the banks and (b) at least a second one of the tiles associated with a second of the banks. In contrast, Hoogenboom appears to be silent regarding (i) a DRAM 22 having multiple banks and (ii) a list associating the pixels within one of the rectangles 22 to multiple banks of a memory. Therefore, Hoogenboom does not appear to disclose or suggest generating a list associating each one of the tiles to a corresponding one page of a plurality of pages in a corresponding one bank of a plurality of banks in a memory such that each one of the picture segments has (a) at least a first one of the tiles associated with a first of the banks and (b) at least a second one of the tiles associated with a second of the banks, as presently claimed. Claim 20 provides language similar to claim 11.

As such, claims 11 and 20 are fully patentable over the cited reference and the rejection should be withdrawn.

Claim 19 depends from claim 11, which is now believed to be allowable. As such, the dependent claim is fully patentable over the cited reference and the rejection should be withdrawn.

CLAIM REJECTIONS UNDER 35 U.S.C. §103

The rejection of claims 1-10 and 12 under 35 U.S.C. §103(a) as being unpatentable over Hoogenboom in view of Howe '865 has been obviated in part by amendment, is respectfully traversed in part, and should be withdrawn.

The rejection of claims 13-16 and 18 under 35 U.S.C. §103(a) as being unpatentable over Hoogenboom in view of Newman et al. '288 (hereafter Newman) has been obviated in part by amendment, is respectfully traversed in part, and should be withdrawn.

The rejection of claim 17 under 35 U.S.C. §103(a) as being unpatentable over Hoogenboom in view of Newman and Bateman, US Pub. No. US 2004/0075750, has been obviated in part by amendment, is respectfully traversed in part, and should be withdrawn.

Hoogenboom concerns a method and apparatus for efficient addressing of DRAM in a video decompression processor (title). Howe concerns a method and circuit for fetching a 2-D reference picture area from an external memory (title). Newman concerns a virtual

memory management and allocation arrangement for digital data processing system (title). Bateman concerns a flexible memory management for video and still image data in a digital camera (title).

Claim 1 provides a decode processor configured to (i) divide a first picture from a video signal into a plurality of picture segments and (ii) divide each one of the picture segments into a plurality of tiles. In contrast, Hoogenboom appears to be silent regarding the rectangles 72 (alleged claimed picture segments) being further subdivided into tiles. Howe does not appear to cure the deficiency of Hoogenboom. Therefore, Hoogenboom and Howe, alone or in combination, do not appear to teach or suggest a decode processor configured to (i) divide a first picture from a video signal into a plurality of picture segments and (ii) divide each one of the picture segments into a plurality of tiles, as presently claimed.

Claim 1 further provides that the decoder processor is configured to (iii) generate a list associating each one of the tiles to a corresponding one page of a plurality of pages in a corresponding one bank of a plurality of banks in a first memory such that each one of the picture segments has (a) at least a first one of the tiles associated with a first of the banks and (b) at least a second one of the tiles associated with a second of the banks. In contrast, Hoogenboom appears to be silent regarding (i)

a DRAM 22 having multiple banks and (ii) a list associating the pixels within one of the rectangles 22 to multiple banks of a memory. Howe does not appear to cure the deficiency of Hoogenboom. Therefore, Hoogenboom and Howe, alone or in combination, do not appear to teach or suggest a decoder processor configured to (iii) generate a list associating each one of the tiles to a corresponding one page of a plurality of pages in a corresponding one bank of a plurality of banks in a first memory such that each one of the picture segments has (a) at least a first one of the tiles associated with a first of the banks and (b) at least a second one of the tiles associated with a second of the banks, as presently claimed. As such, claim 1 is fully patentable over the cited references and the rejection should be withdrawn.

Claim 2-10 and 12-18 depends from claims 1 and 11, which are now believed to be allowable. As such, the dependent claims are fully patentable over the cited reference and the rejections should be withdrawn.

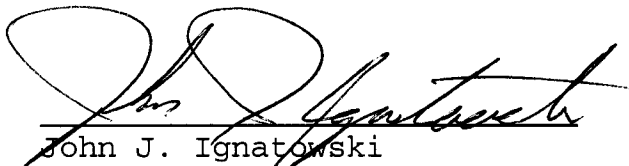
Accordingly, the present application is in condition for allowance. Early and favorable action by the Examiner is respectfully solicited.

The Examiner is respectfully invited to call the Applicant's representative between the hours of 9 a.m. and 5 p.m. ET at 586-498-0670 should it be deemed beneficial to further advance prosecution of the application.

If any additional fees are due, please charge Deposit
Account No. 12-2252.

Respectfully submitted,

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